

TSDSI-WWRF JOINT ONLINE WORKSHOP

RECONFIGURABLE INTELLIGENT SURFACES (RIS)

17 September 2024

Supported by



**Reconfigurable Intelligent Surfaces:
Sharing Some Experience**

by

Arzad Alam Kherani, IIT Bhilai

A motivating case: Industrial IoT

Multiple wireless technologies working in parallel

Channel Estimation
Algorithms at sender and receiver

Intelligence at sender and receiver



What if we can **control the channel** and **leave the end-devices dumb**

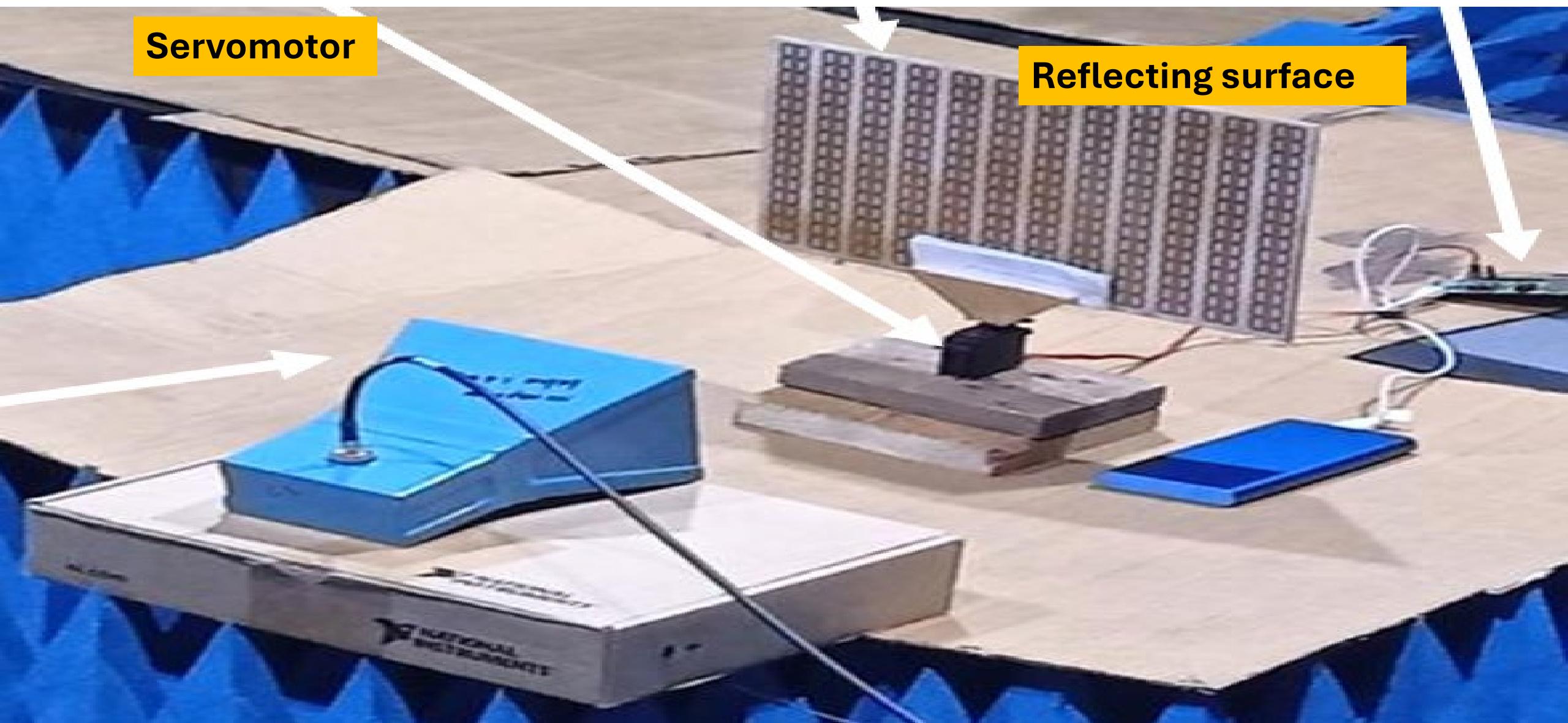
Sensor manufacturers forced to work with, and choose the “right” wireless technology

Interoperability

Difficult for smaller wireless technology providers

Significantly high end-device cost

A Simple “Smart” Radio Environment (our first attempt at achieving SRE)



Katha {HD} - Naseeruddin Shah - Deepti Naval - Farooq Shaikh - Full Hindi Movie



shemaroo



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shemaroo

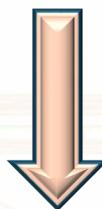


Very Promising

Smart Radio Environments and Reconfigurable Intelligent Surfaces

A Smart Radio Environment (SRE) is a wireless environment that is turned into a smart reconfigurable space and that plays an active role in transferring and processing information

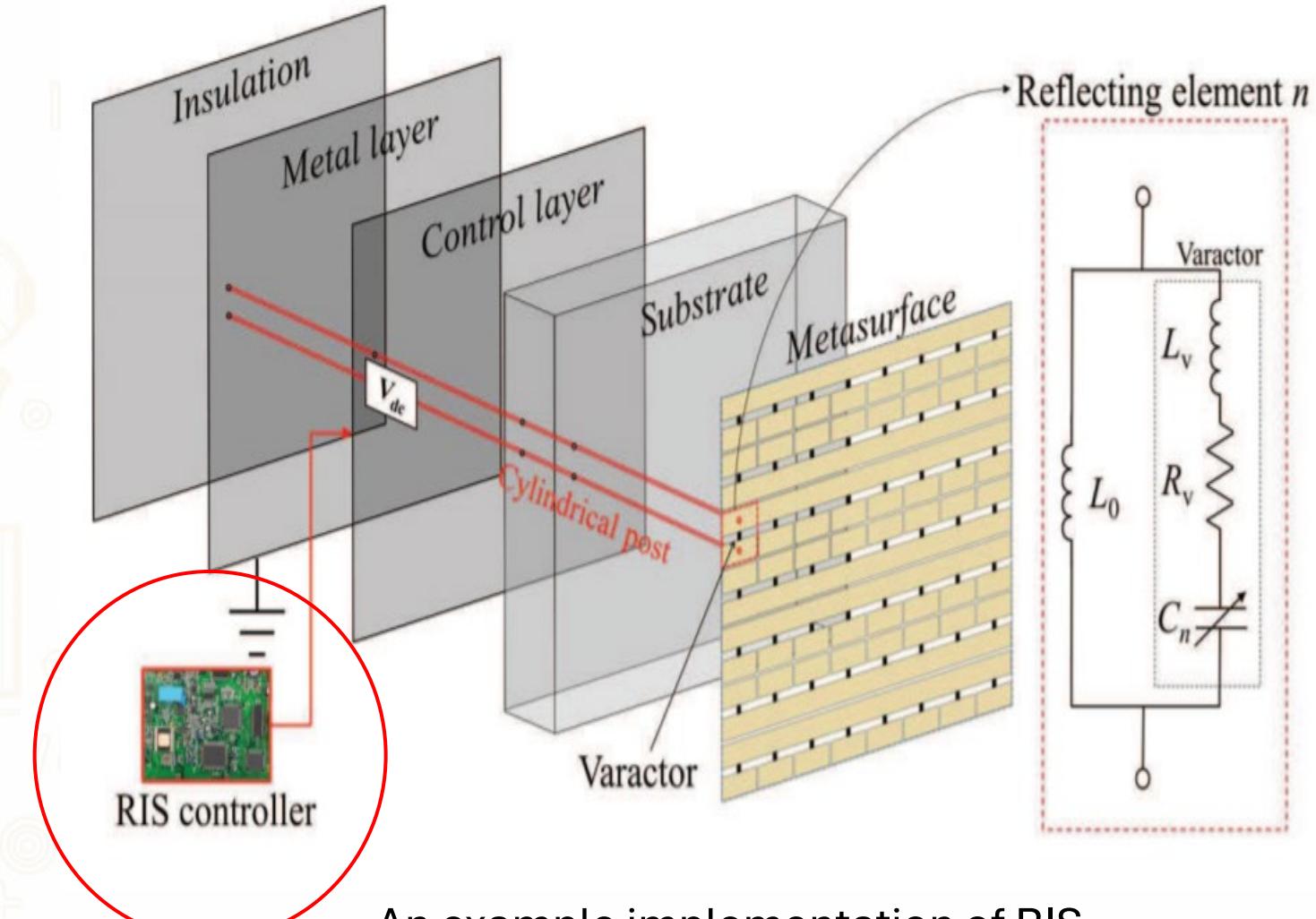
Source: <https://hal.archives-ouvertes.fr/hal-02395877/document>



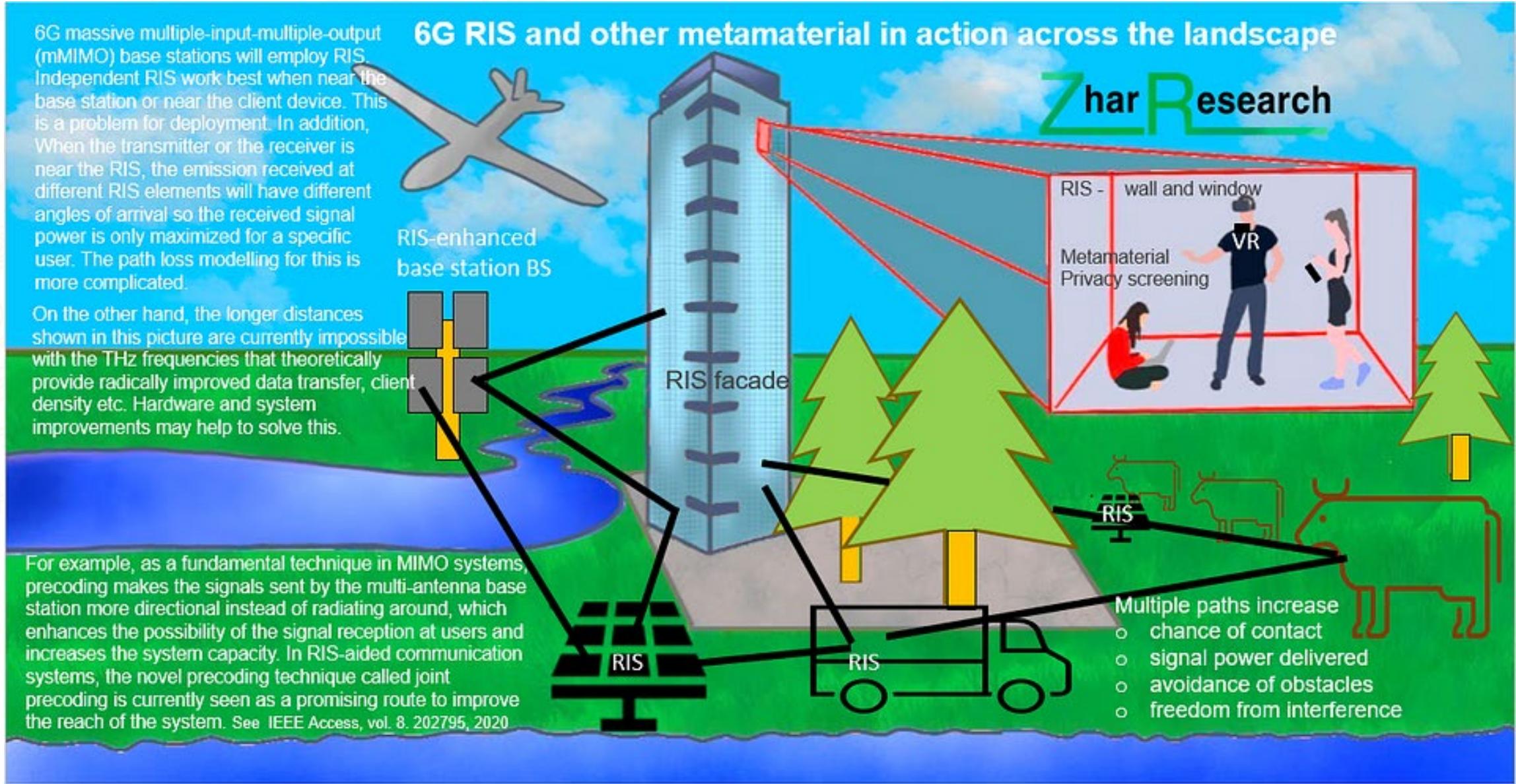
Can be achieved using

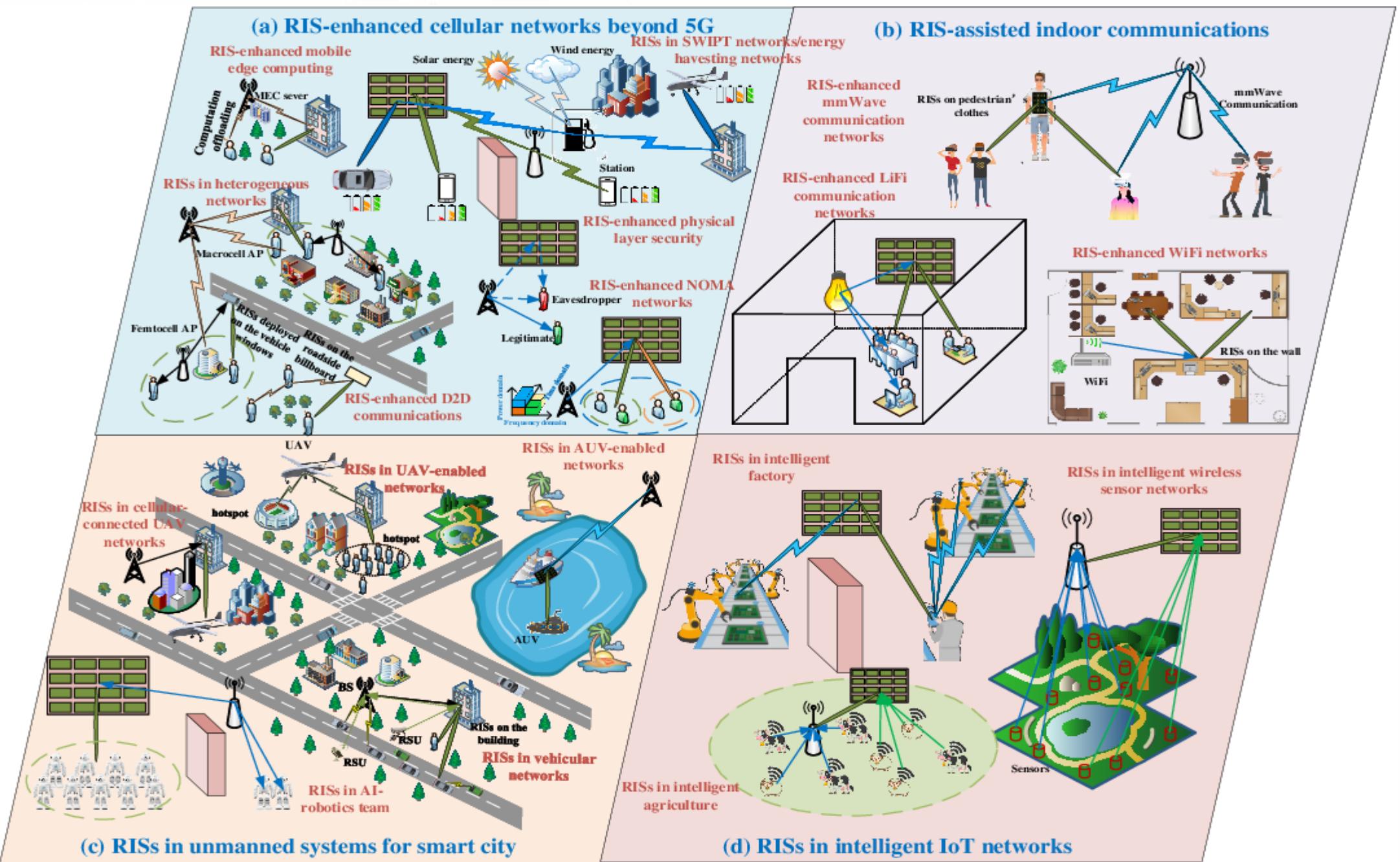
A Reconfigurable Intelligent Surfaces (RIS) is an artificial surface, made of electromagnetic material, that is capable of customizing the propagation of the radio waves impinging upon it.

(Source: <https://arxiv.org/pdf/1908.08747.pdf>)



An example implementation of RIS.





WIRELESS SENSOR NETWORKS THESIS TOPICS

- *Energy-Efficient Routing Protocols in WSNs*
- *Security Mechanisms for WSNs*
- *Application of Machine Learning in WSNs*
- *Integration of WSNs with IoT for Smart Cities*
- *Underwater Wireless Sensor Networks (UWSNs)*
- *WSNs for Precision Agriculture*
- *Health Monitoring Using WSNs*
- *Cross-Layer Optimization Techniques for WSNs*
- *Blockchain for Data Integrity in WSNs*



Students/Staff: Saksham, Kaushik, Ashutosh, Sai, Shubhika, Smriti, Vishwajeet, Amit, Khushboo, Bhavesh

and, with COMET Foundation (IIIT Bangalore)

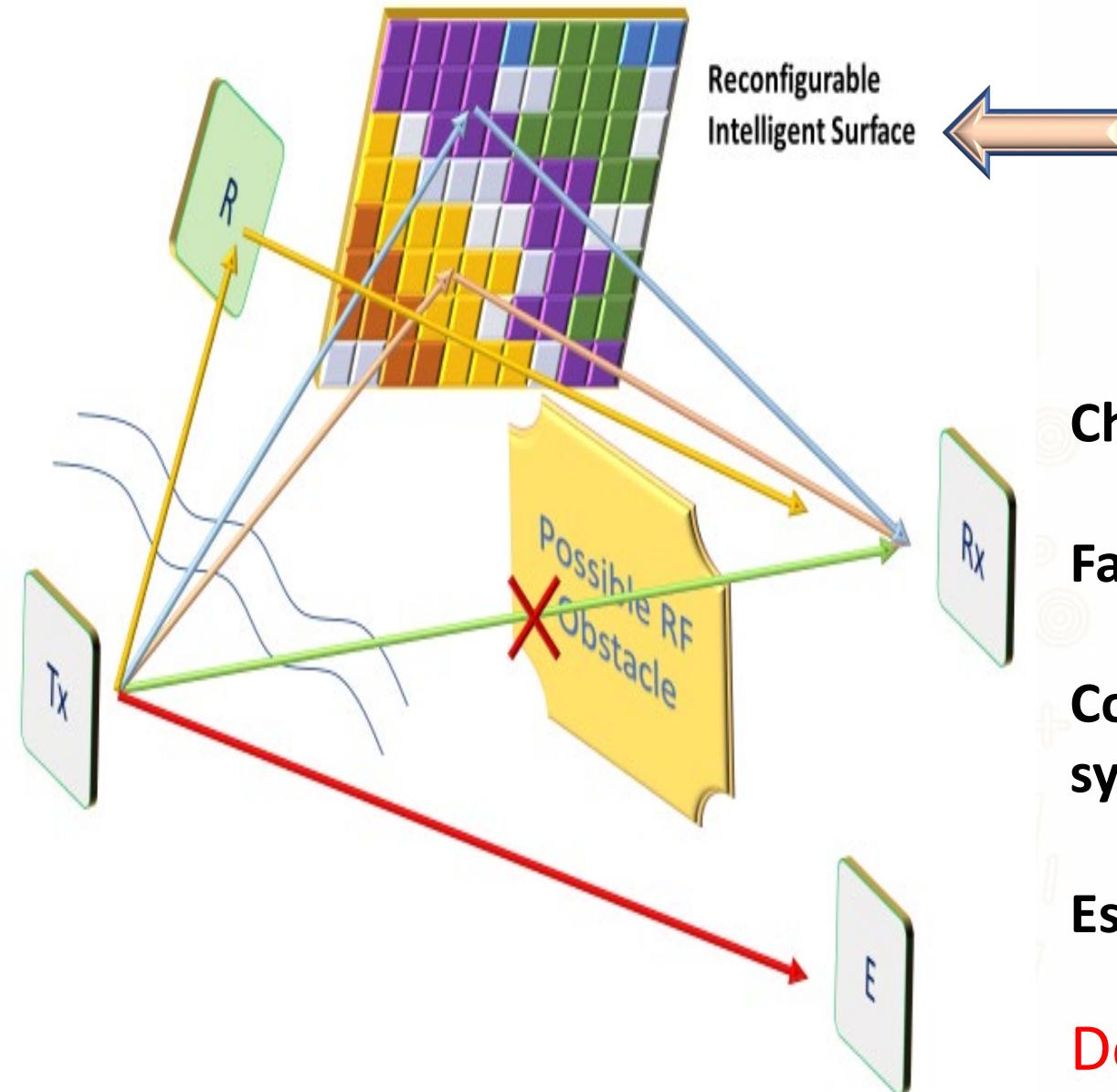
Brejesh Lall (IIT Delhi), Vimal Bhatia (IIT Indore), Rajarshi Mohapatra (IIIT Naya Raipur), Sanjeev Sharma (IIT BHU), Samrat Mukhopadhyay (IIT Dhanbad), Ravi Panwar (IIT BHU), Soumava Mukherjee (IIT Jodhpur), Priyanka Das (IIIT Bangalore),

and, Jishnu, Gautam from Tejas Networks.

and, the impressive pool of TSDSI members and Secretariat (for the standardization aspect)
Ably supported by Prof. Debabrata Das, Milind Gandhe, Sridhar P., Amudeeshan
(Apologies if I missed someone)

Views presented are my own and based on publicly available information.





One controller for the overall radio
environment configuration

Challenges:

Fabrication

Control and Communication capability over RIS systems

Estimation of ongoing performance to help control

Does standardization even make sense?

- Exactly what should be standardized?

System Architecture and Ownership

Channel Estimation

(IIT Bangalore)

Desired control is provided at RF level abstraction (beam width, etc)

Generic Controller

(IIT Indore, IIT BHU, IIIT-NR, IIT Delhi, IIT Bhilai)

Proprietary Interfaces

RIS (hardware)

(IIT BHU, IIT Jodhpur, IIT Bhilai)

RF level abstraction translated to specific RIS (provided by the vendor).

RIS Specific controller

(IIT BHU, IIT Jodhpur, IIT Bhilai)

Standardized Interface (IIT Bhilai)

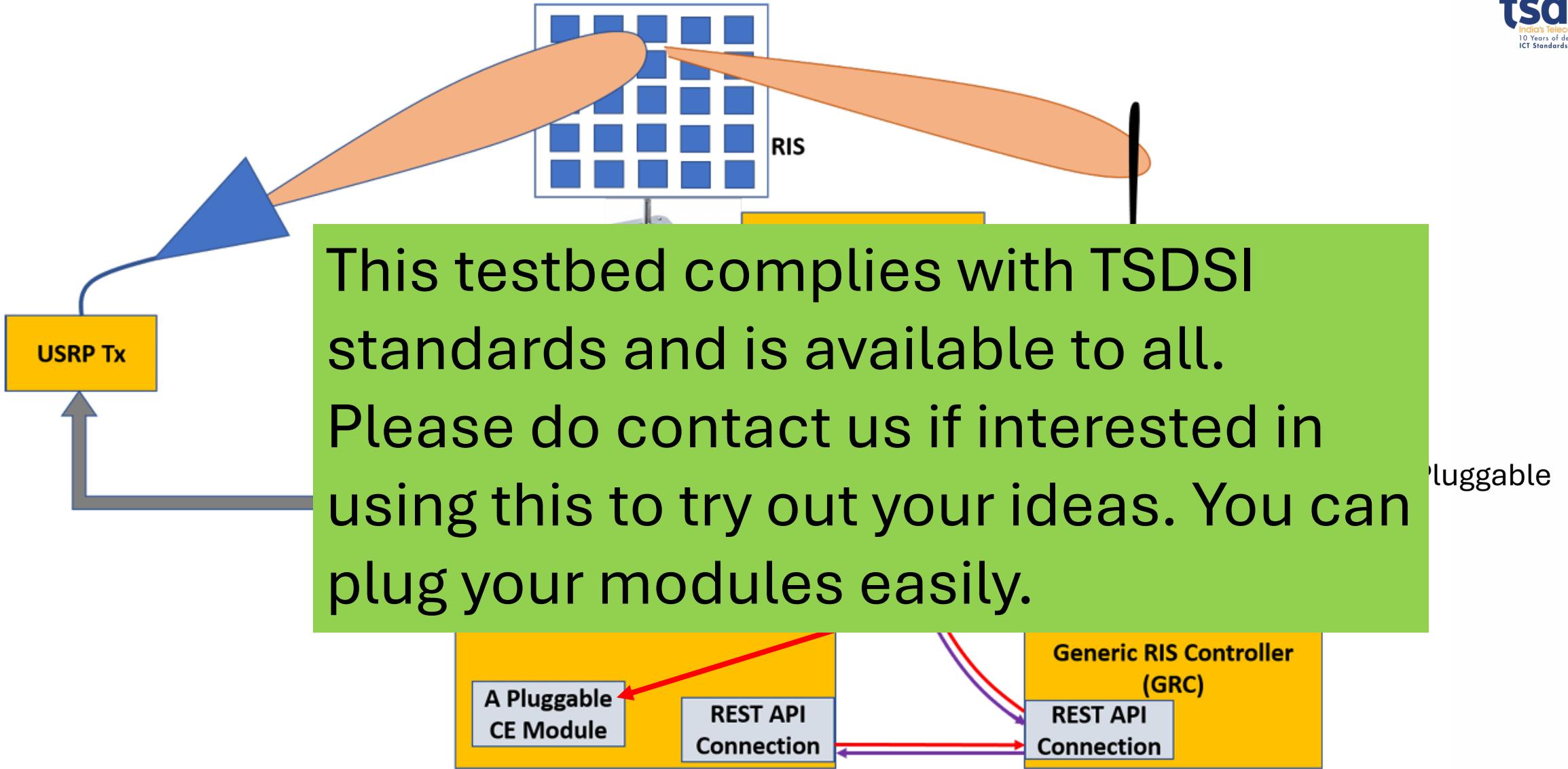
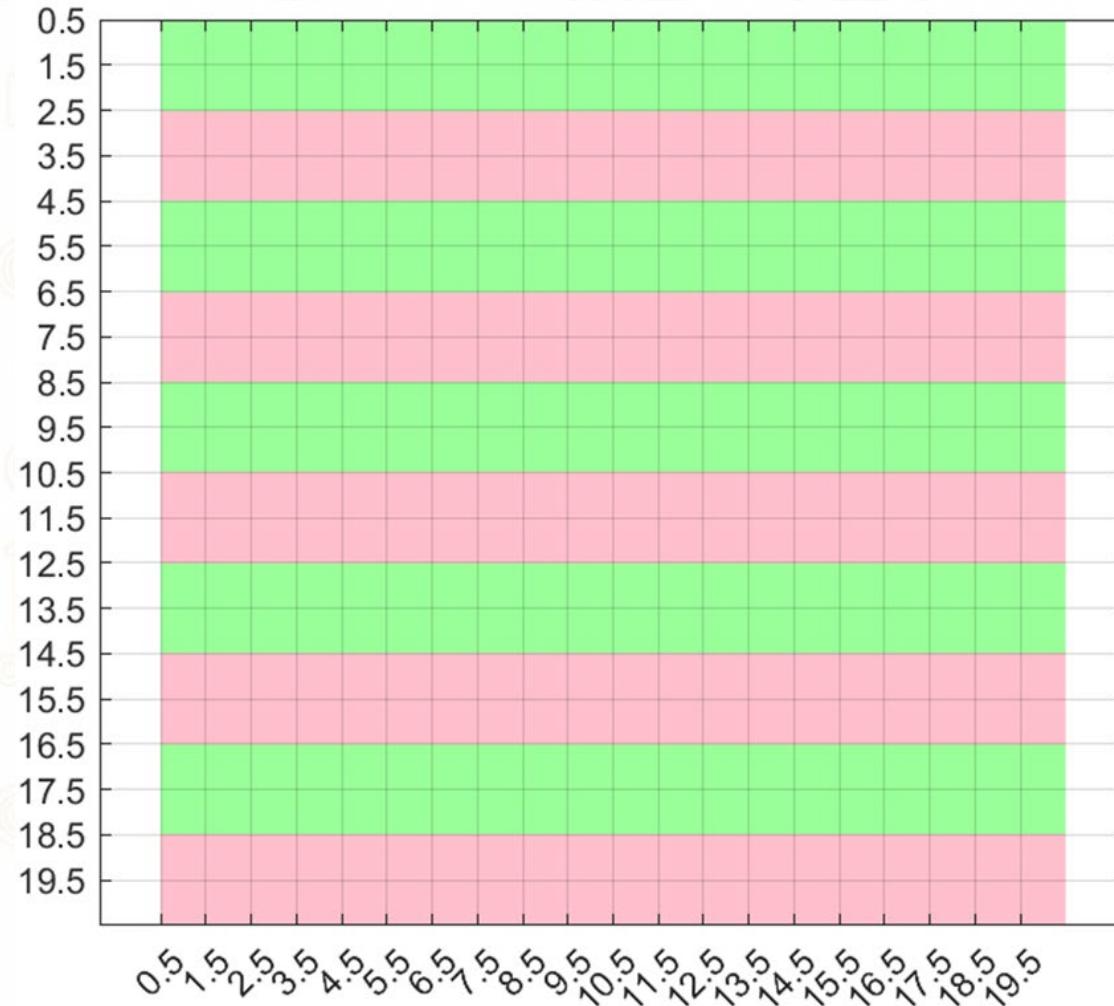
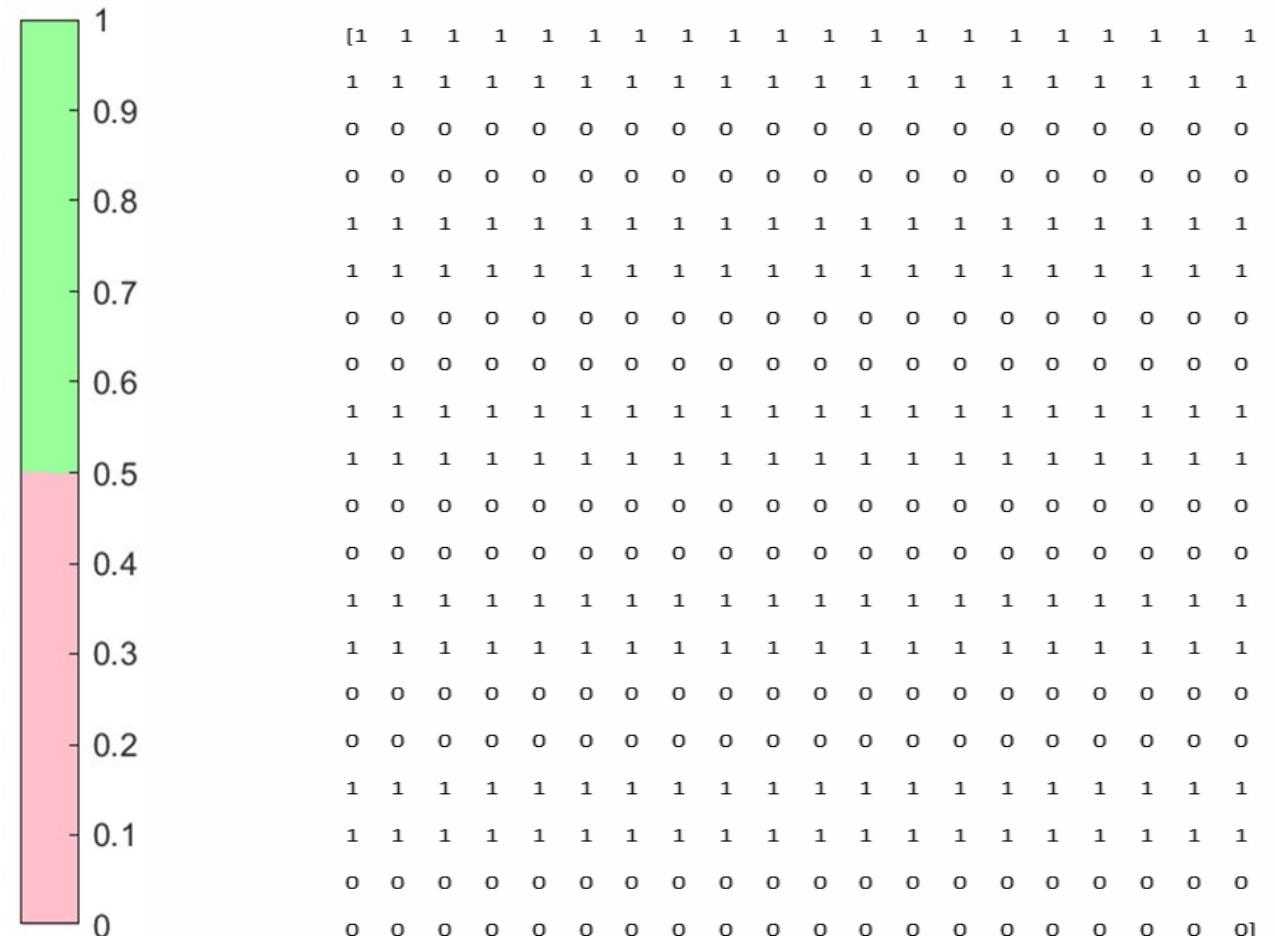


Fig. 1: System model for RIS assisted communication.

RIS-Specific Controller: A Lookup Table Approach



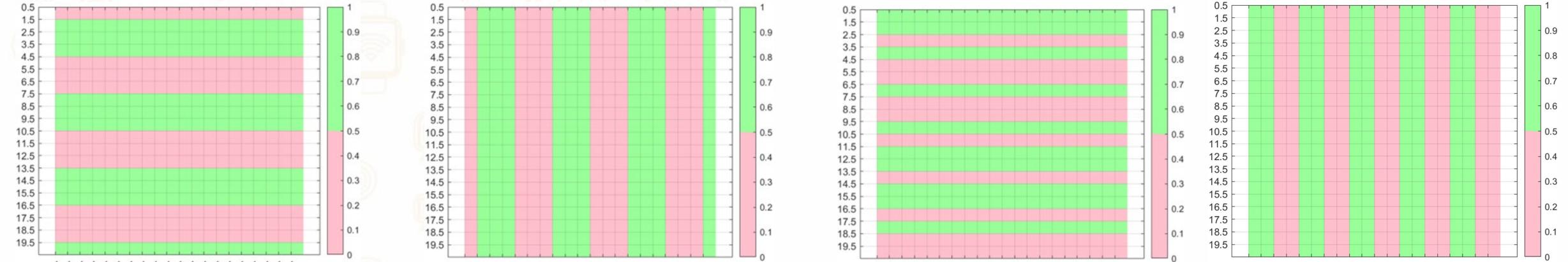
The pattern in theta plane



Matrix generated for **30-degree** reflection

Acknowledgement: Soumava and Anjana, IIT Jodhpur

Reflection Patterns for Various Angles in Theta and Phi Planes

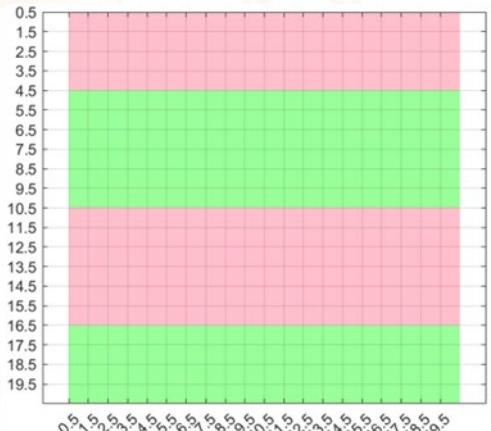


$\Theta=20^\circ$

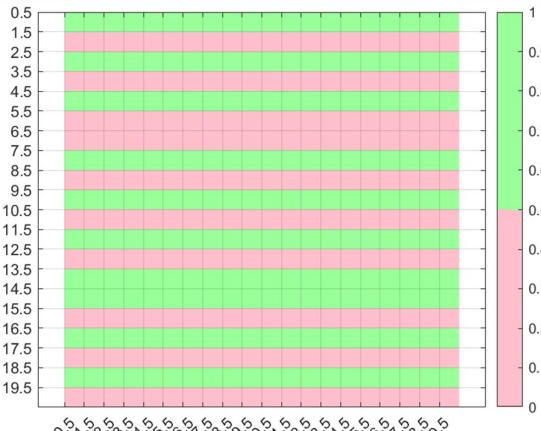
$\phi=20^\circ$

$\Theta=45^\circ$

$\phi=30^\circ$



$\Theta=10^\circ$



$\Theta=60^\circ$

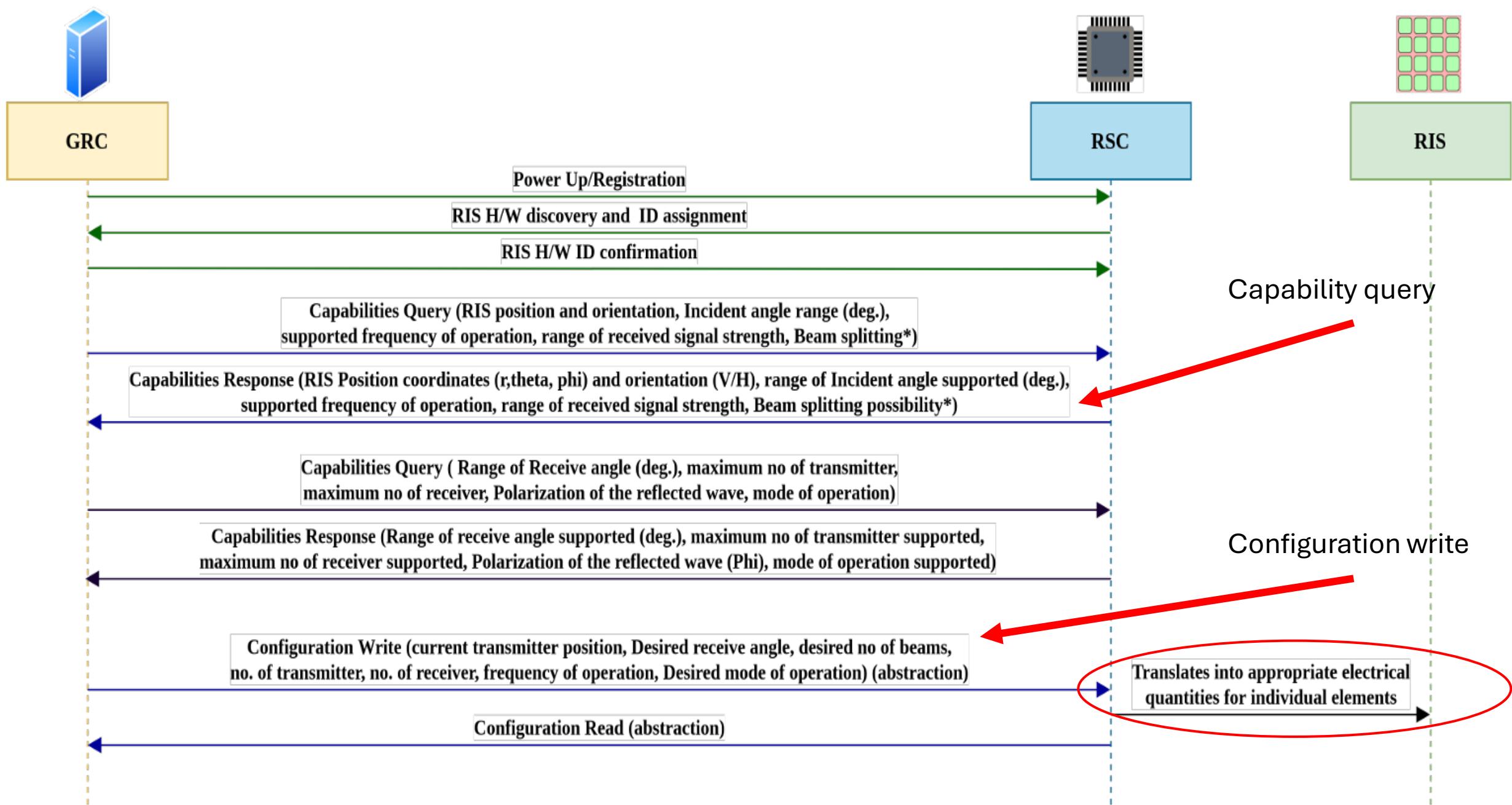


Fig. 4: Message flow sequence between GRC, RSC and RIS.

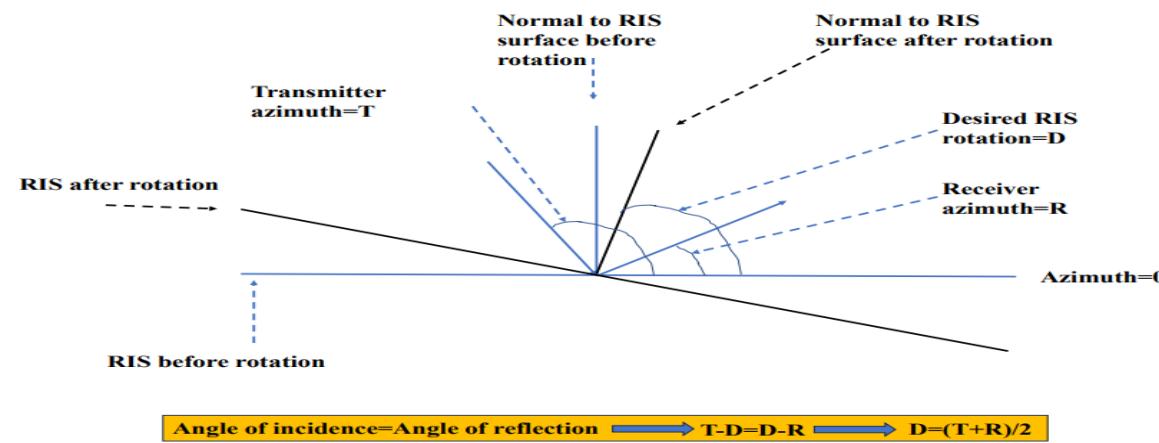
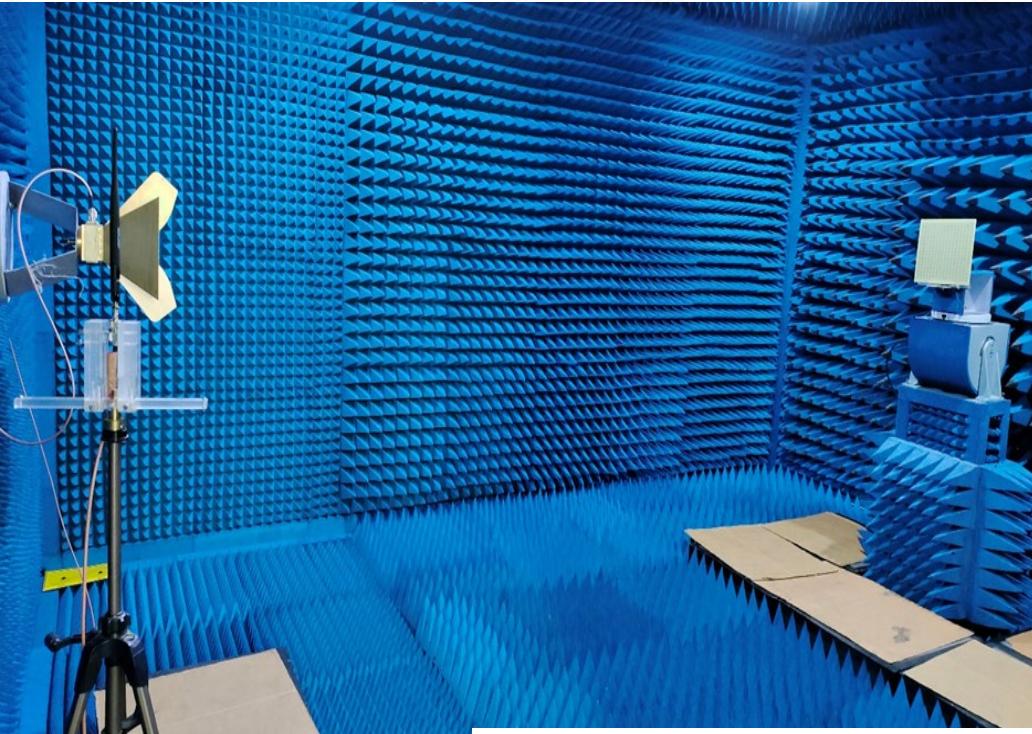


Fig. 6: Rotation of RIS required for beamforming at the desired location.

How to use these interfaces?

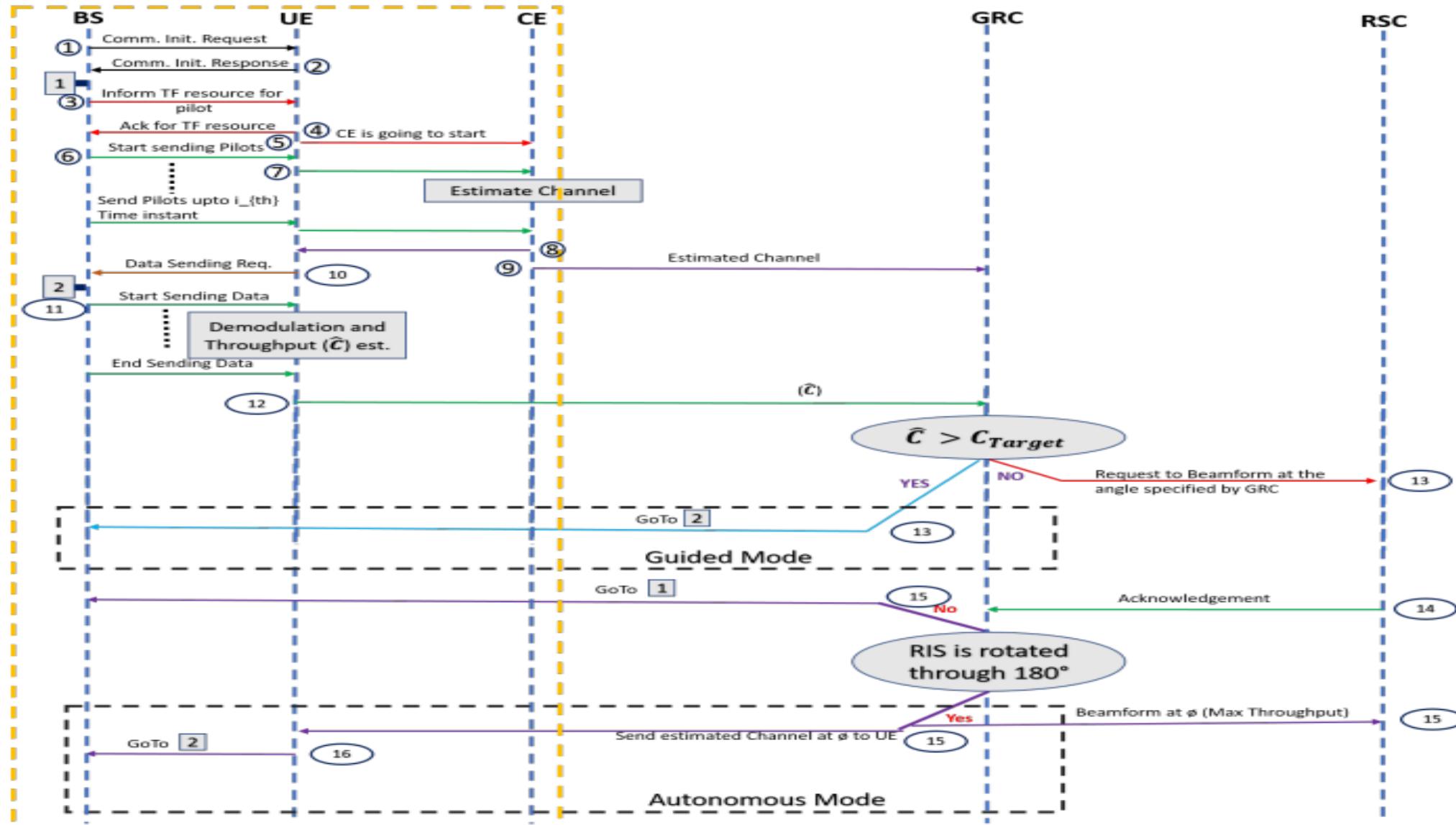


Fig. 3: Message flow between CE and GRC.

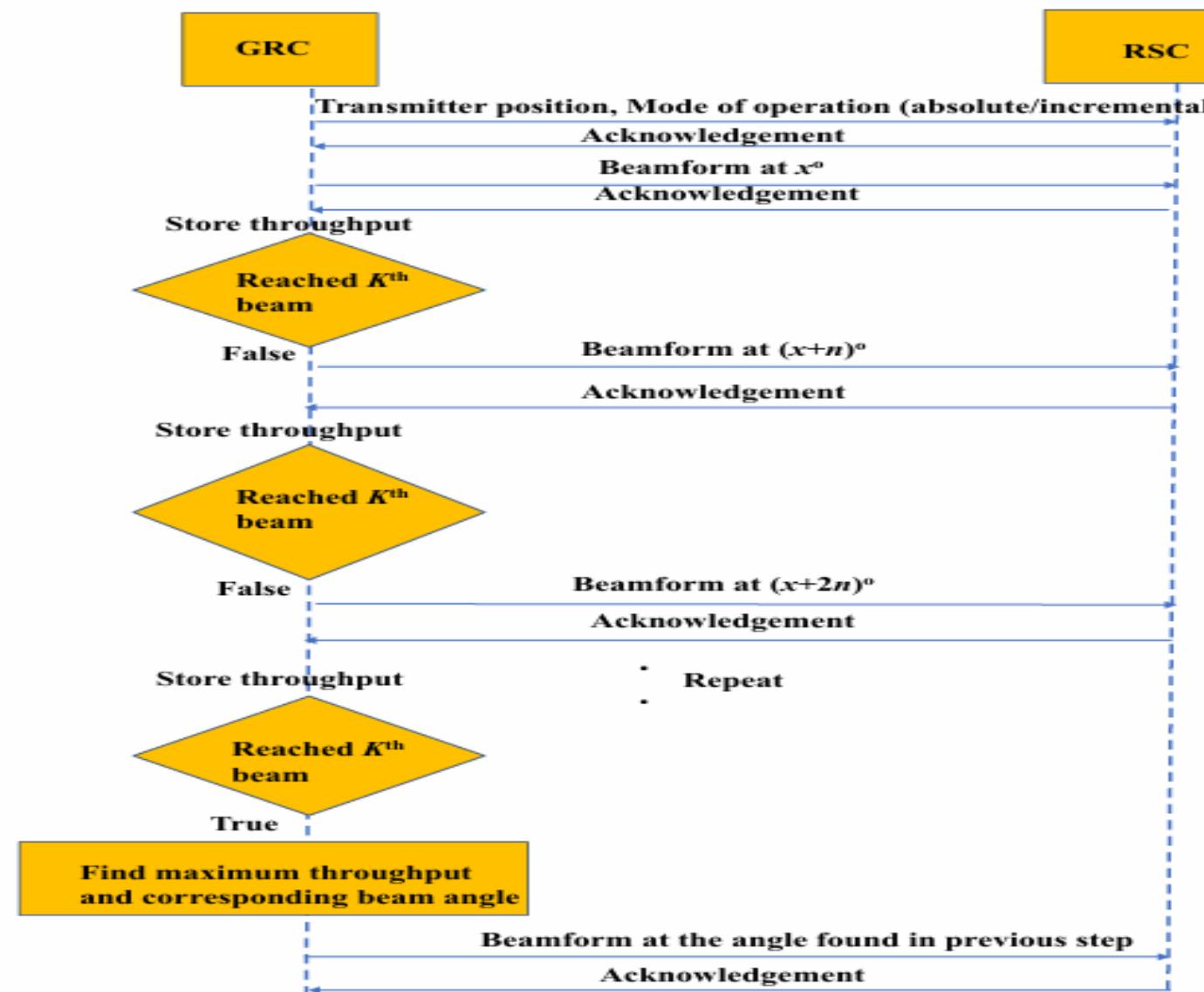


Fig. 5: GRC algorithm to find maximum power and corresponding beamforming angle.

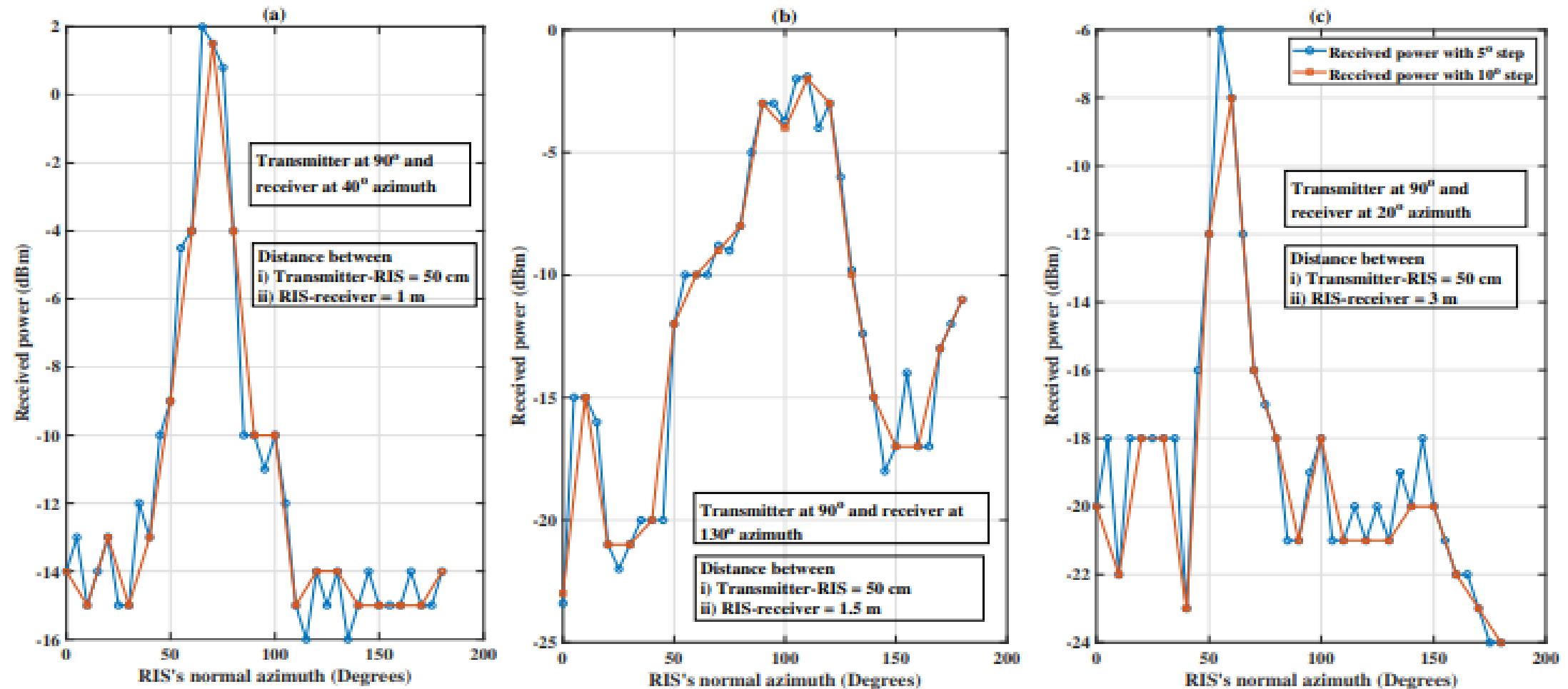


Fig. 7: Received power variation versus RIS's normal azimuth varying at 5° and 10° stepsize with transmitter at 90° azimuth and receiver at (a) 40° azimuth (b) 130° azimuth (c) 20° azimuth.

TSDSI STD 5003 V1.0.0

TSDSI STD 5003 V1.0.0

**Methods and Interface
Design for RIS-assisted
Communication Systems**

August 2024

Great support from TSDSI members and Secretariat.

Also covers RIS with Localized Control Unit (tighter integration between BS and RIS)

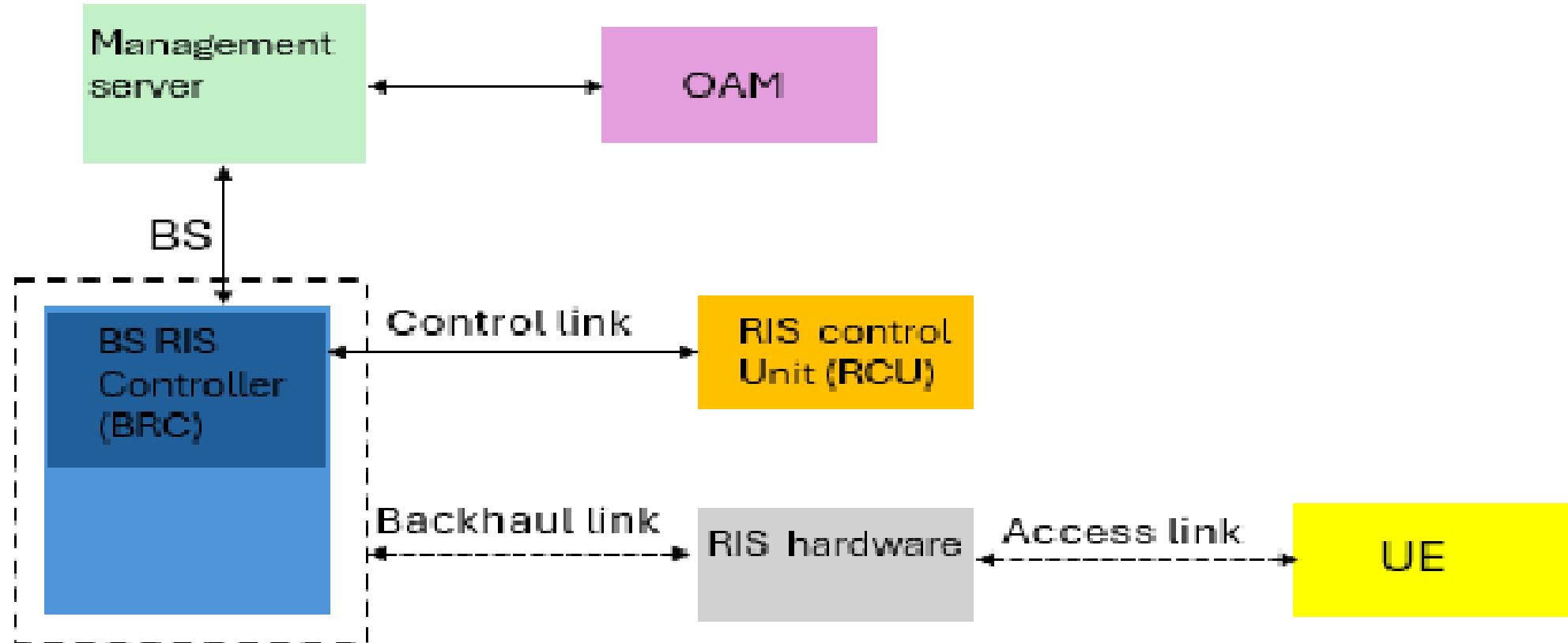


Figure 3 Architecture of RIS with localized control unit

ETSI:

- Three whitepapers published by RIS ISG.
- Ongoing activities on
 - System/link performance, spectrum, co-existence, and security.
 - Technological challenges in terms of deploying RIS as a new network node.
 - impacts to network architecture, protocol architecture, and framework of RIS
- controlling
- requirements and potential impact to specifications to support RIS

RISTech Alliance(RISTA):

- Whitepaper available

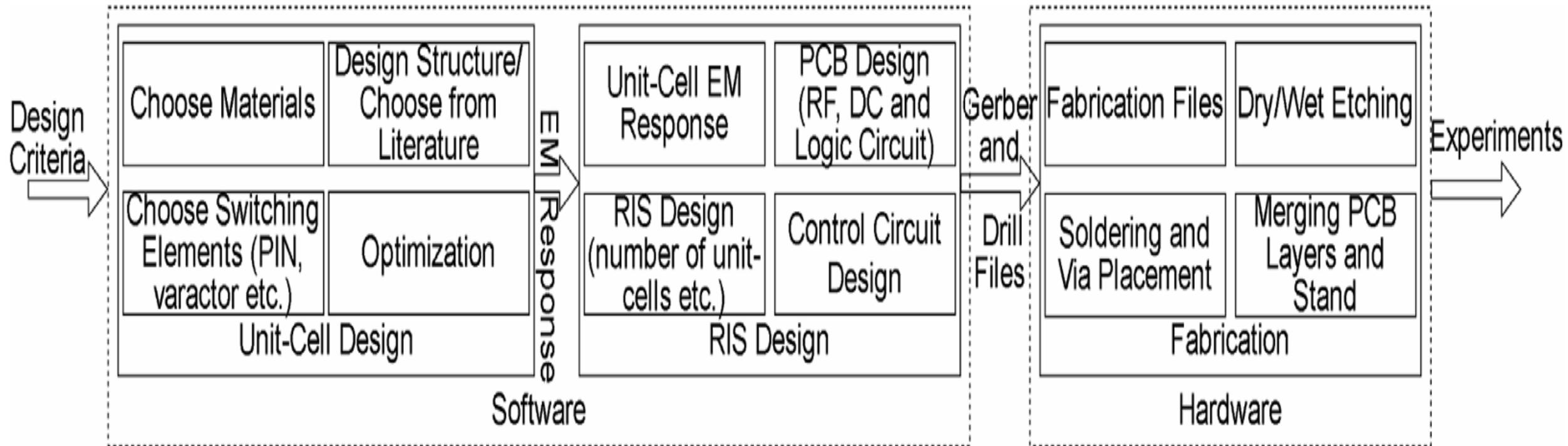
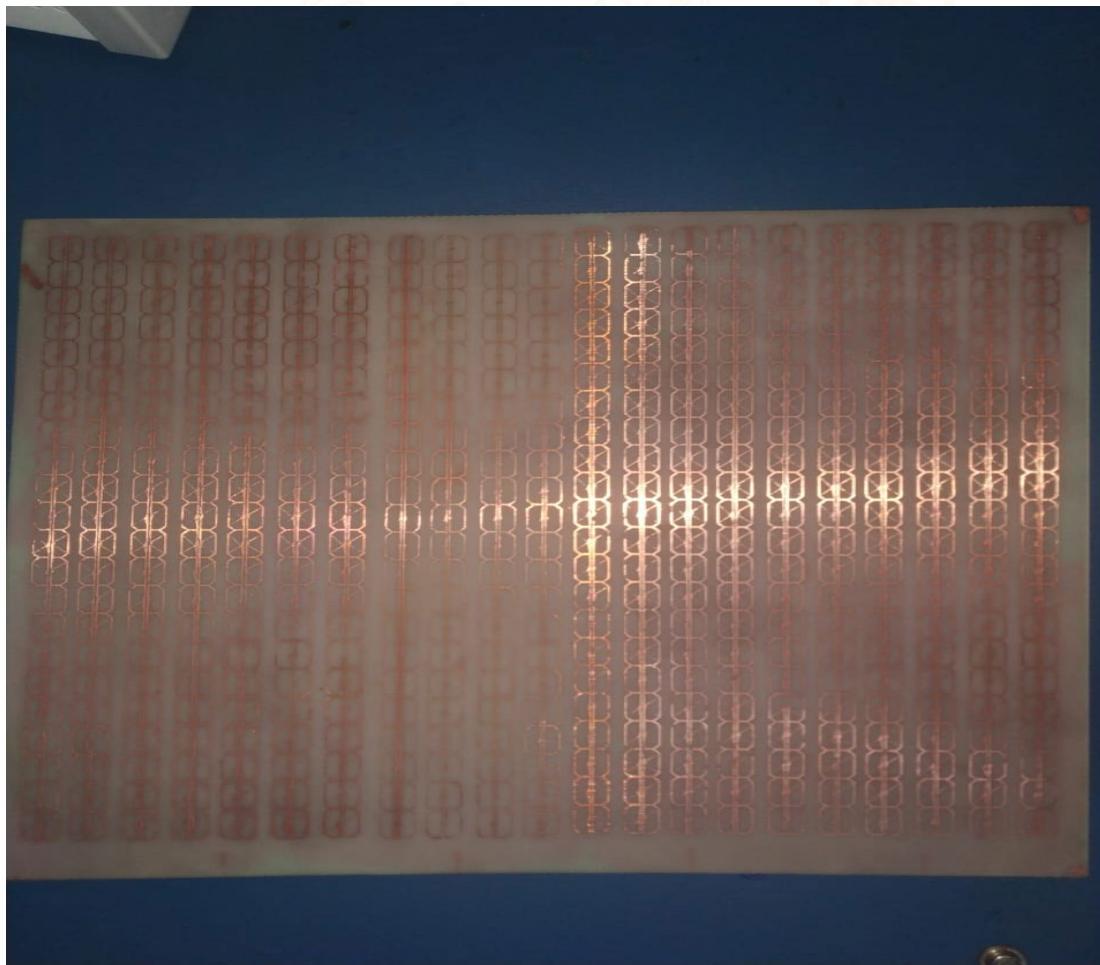


Fig. 1: Design to Fabrication, complete workflow summary.

Source: A Guide for RIS Fabrication for Quick Prototyping in Lab Settings Using Low Cost Fabrication Techniques

Passive RIS Fabricated



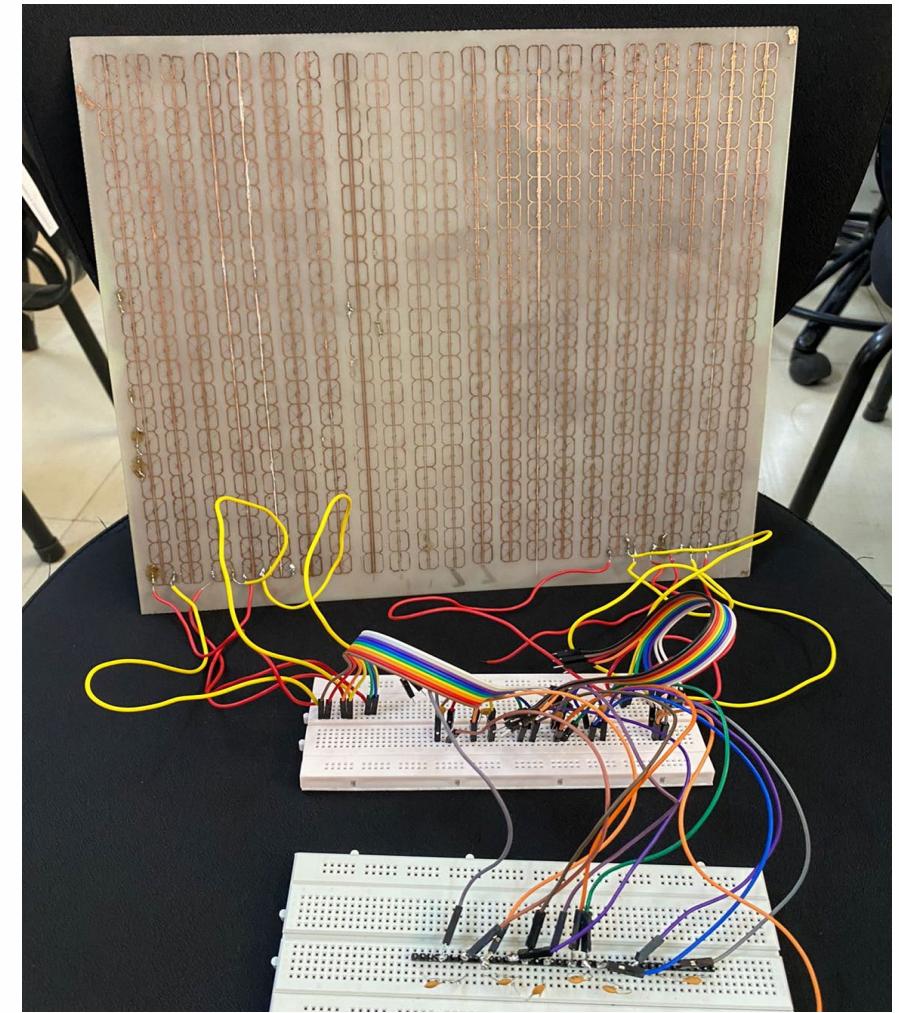
Fabricated RIS

Based on University of Surrey design.

<https://openresearch.surrey.ac.uk/esploro/outputs/99623165302346?skipUsageReporting=true>

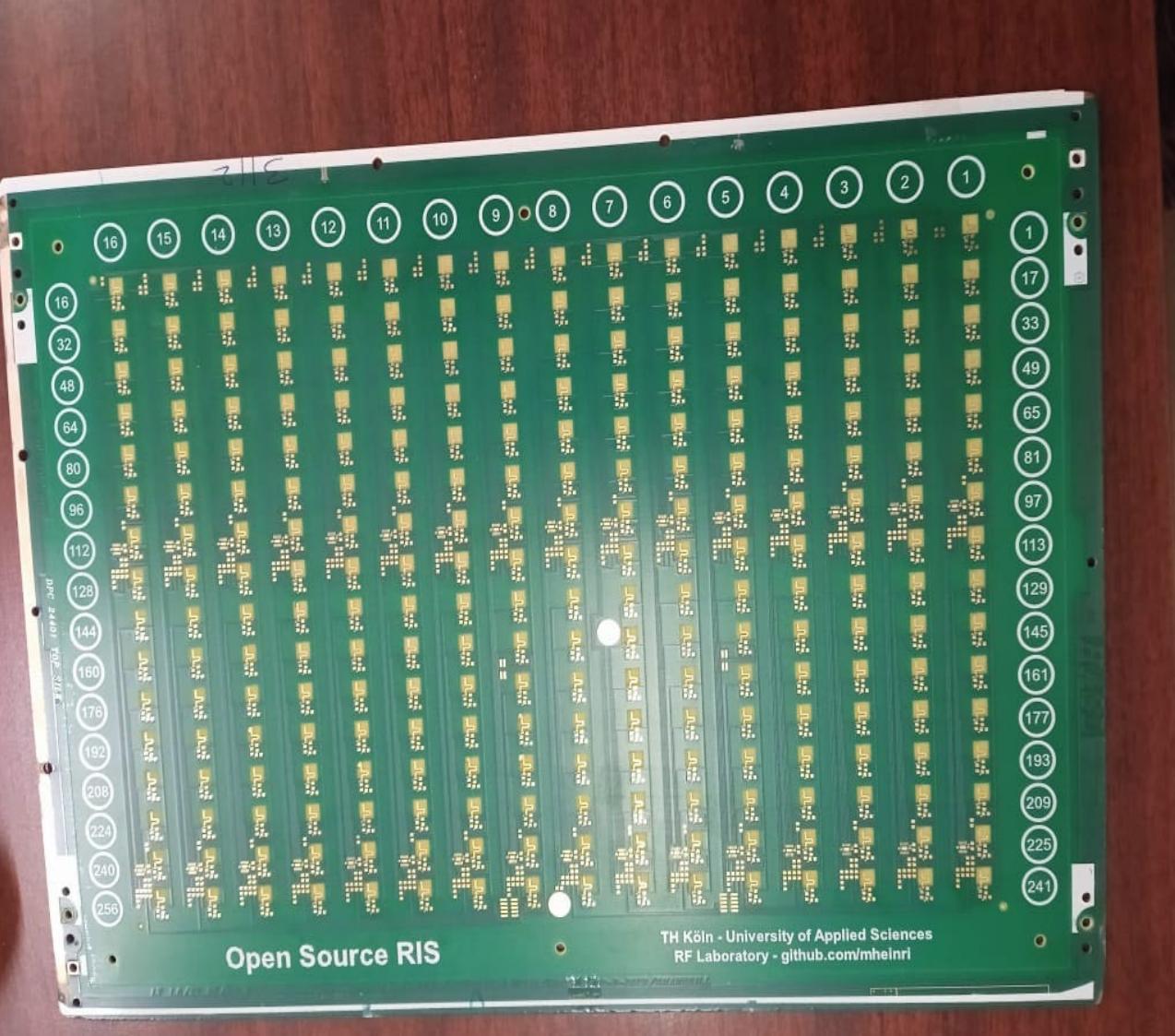
17 September 2024

TSDSI-WWRF Online workshop on RIS

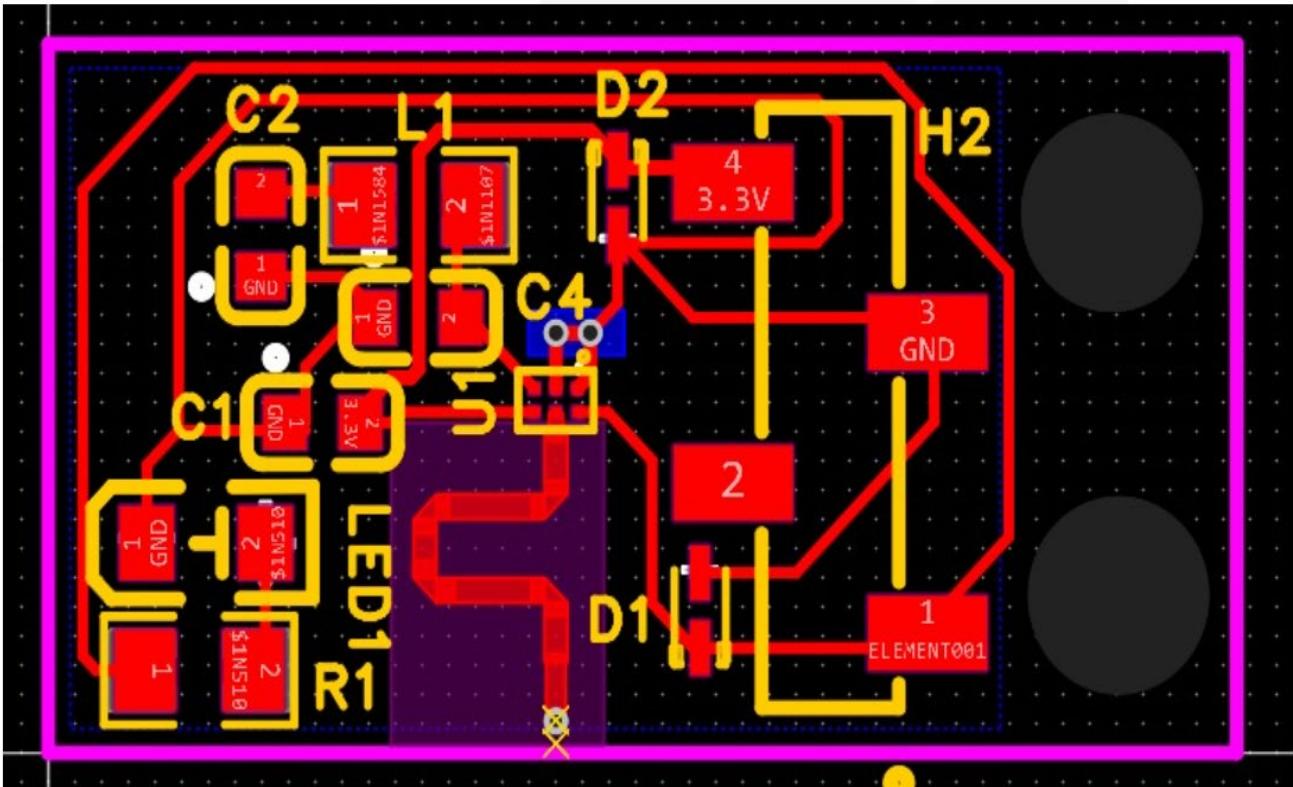


RIS with parallel capacitive setup (manual switching, to mimic varactor diodes)

Active RIS Fabricated and Control Circuitry Designed



Designing individual elements (for flexibility)



Designator

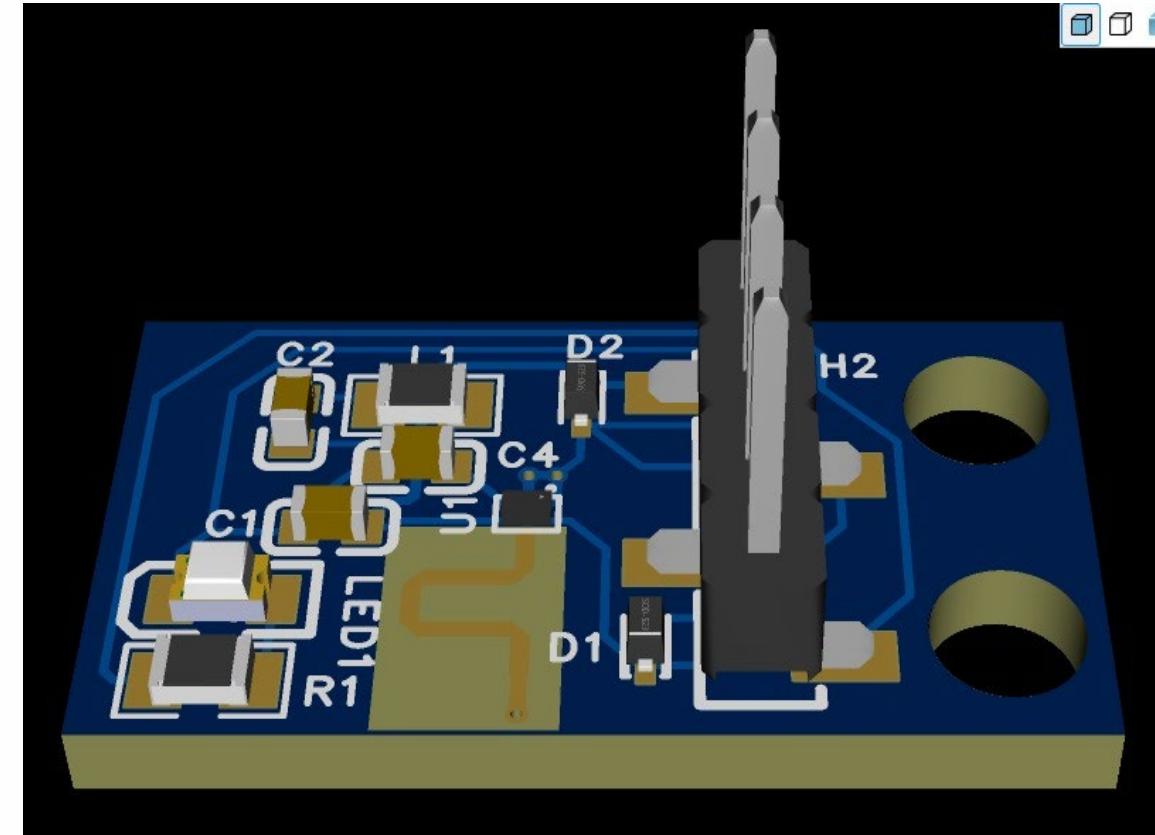
U1

LED1

R1/L1

D1/D2

C1/C2/C4



Device

BGS12PL6E6327XTSA1_C2939943 (18-20 Rs)

17-21SURC/S530-A3/4T (2.5 Rs)

RC0805JR-071KL (8 Rs.)

ESD5Z3V3_C502545 (5 Rs)

CC0603KRX7R7BB104 (1.5 rs)

Extensions that we are working on

1. Static behaviour of RIS elements with random deployment
2. ISAC-capable RIS Element Design (patent process ongoing)

3. ...

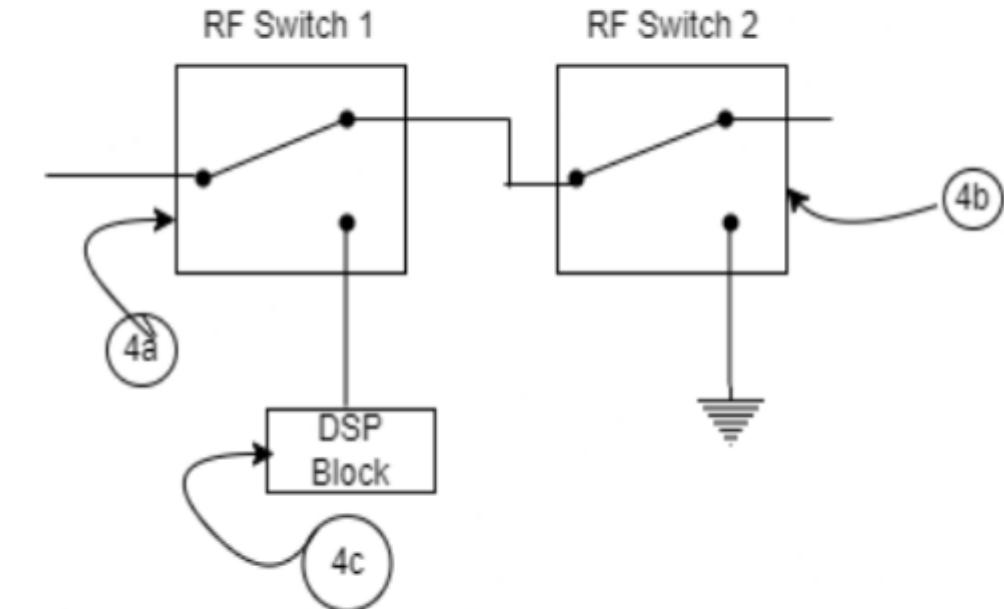


Figure 3: Switching element schematic

Thank You
arzad.alam@iitbhilai.ac.in